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1. **[\[quant-ph/9702033\] Five Quantum Register Error Correction Code For .....](#)**

... Five Quantum Register **Error Correction Code** For Higher Spin Systems. Authors: HF Chau  
Revised version, to appear in Phys.Rev.A (Rapid Communications). ...  
[arxiv.org/abs/quant-ph/9702033](http://arxiv.org/abs/quant-ph/9702033) - 3 KB

2. **[\[quant-ph/9602019\] Perfect Quantum Error Correction Code](#)**

From: Raymond LaFlamme &lt;laf@t6-serv.lanl.gov&gt; Date: Tue, 27 Feb 1996 10:27:56 -0700  
Perfect Quantum **Error Correction Code**. ...  
[arxiv.org/abs/quant-ph/9602019](http://arxiv.org/abs/quant-ph/9602019) - 4 KB

3. **[RDS Error Correction](#)**

**Error Correction**. Since the RDS signal is digital, there is a capacity to include some digital **error correction code** in the signal. ...  
[murray.newcastle.edu.au/users/staff/eemf/ELEC351/SProjects/Hopper/error.htm](http://murray.newcastle.edu.au/users/staff/eemf/ELEC351/SProjects/Hopper/error.htm) - 4 KB

4. **[Memory Error Detection and Correction](#)**

**Memory Error Detection and Correction**. An Introduction to Parity and ECC. Someone asked:  
for the info. I guess my confusion ...  
[www.brouhaha.com/~eric/computers/ecc.html](http://www.brouhaha.com/~eric/computers/ecc.html) - 8 KB

5. **[ECC - Error Correction Code](#)**

**ECC [Error Correction Code] [Error Checking and Correction]. Error Correction Code** (engl.) - zur Fehlerkorrektur; **Error** Checking ...  
[ig.cs.tu-berlin.de/ap/rg/1998-04/glossar/e-terms/ecc.html](http://ig.cs.tu-berlin.de/ap/rg/1998-04/glossar/e-terms/ecc.html) - 3 KB

6. **[SECC - Secret Error Correction Code](#)**

**SECC [Secret Error Correction Code]**. Secret **Error Correction Code** (engl.) - geheimer **Code** zur Fehlerkorrektur: SECC stammt vom **Error** ...  
[ig.cs.tu-berlin.de/ap/rg/1998-04/glossar/s-terms/secc.html](http://ig.cs.tu-berlin.de/ap/rg/1998-04/glossar/s-terms/secc.html) - 3 KB

7. **[3.1.6 Error correction/detection techniques](#)**

... Cross Interleaved Reed Solomon **Code** (CIRC). CIRC is an **error correction code** developed  
for CDs. Using the basic CIRC form ...  
[uranus.ee.auth.gr/report/eng/part1/chapt-03/3-1-6.html](http://uranus.ee.auth.gr/report/eng/part1/chapt-03/3-1-6.html) - 3 KB

## 8. 20 Fault Tolerance & Memory Hierarchy

... Minimum Hamming distance between any two **code** word  $\times$  **Error detection code** can detect the **code** distance  $d \geq p+1 \times$  **Error correction code** can correct ...  
[www.ece.cmu.edu/~ece548/handouts/20depend.pdf](http://www.ece.cmu.edu/~ece548/handouts/20depend.pdf) - 0 B

## 9. Using Guide with a JMI/MicroGuider III encoder system

Using Guide with a JMI/MicroGuider III encoder system. Last updated 1 Mar 98. You may have there are some new entries ...  
[www.projectpluto.com/mec.htm](http://www.projectpluto.com/mec.htm) - 16 KB

## 10. Supplement on Parity and Error Correcting RAM

... Repeating each bit four times is the **error correction code** that is simplest to describe that and correct single-bit errors, and can detect double ...  
[oak.cats.ohiou.edu/~piccard/mis300/eccram.htm](http://oak.cats.ohiou.edu/~piccard/mis300/eccram.htm) - 8 KB

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## WEB RESULTS by **Google** (Showing Results 1 - 10 of 84)

### 1. Overview: The Project

... The **Advantages of Relative Addressing** One advantage of this **relative addressing** capa you can backup your projects and map layers to disk and ...  
[www.maxonline.lsu.edu/pages/MFguide/Projects/Projects.pdf](http://www.maxonline.lsu.edu/pages/MFguide/Projects/Projects.pdf)

### 2. CTI Computing: VISUALISATION : HARD WORK MADE EASY

... this approach together with its aesthetic and pedagogic **advantages**. ... computer architectur example, direct (absolute) and indirect (**relative**) **addressing**. ...  
[www.ulst.ac.uk/cticomp/sh\\_kenn.html](http://www.ulst.ac.uk/cticomp/sh_kenn.html) - 17 KB

### 3. C1 1997

... Ray Tube (CRT). [12 marks] b) i) What is the meaning of "**relative addressing**" and what a does it confer? [5 marks] ii) Explain ...  
[www.cs.ucl.ac.uk/student\\_pages/exams/97/c1.ps](http://www.cs.ucl.ac.uk/student_pages/exams/97/c1.ps) - 0 B

### 4. Architecture Support of A Descriptor Computer

... Figure 4: Operand Descriptor Formats. pointer **relative addressing**. ... 00 Direct **Addressin** Pointer **Relative Addressing** 10 to be defined 11 to be defined. ...  
[www.ee.cityu.edu.hk/~clli/research/oo\\_hisc1.ps](http://www.ee.cityu.edu.hk/~clli/research/oo_hisc1.ps) - 0 B

### 5. C:\BELLBO~1\COMPSR&E\HTMFILES\00000296.HTM

... There are a number of additional benefits derived from the scheme adopted, which include **re** **addressing** so that routines can operate anywhere in the store ...  
[research.microsoft.com/~gbell/Computer\\_Structures\\_\\_Readings\\_and\\_Examples/00](http://research.microsoft.com/~gbell/Computer_Structures__Readings_and_Examples/00) - 6 KB

### 6. Data and Instructions

... **Advantages** and Disadvantages of zero address machines. ... a JUMP instruction is most like the current instruction, so if **relative addressing** is used ...  
[cs-alb-pc3.massey.ac.nz/notes/59304/I3.html](http://cs-alb-pc3.massey.ac.nz/notes/59304/I3.html) - 43 KB

### 7. PATHALIAS or The Care and Feeding of **Relative Addresses**

Page 1. PATHALIAS or The Care and Feeding of **Relative Addresses** Peter Honeyman Computer Department Princeton University Princeton ...  
[www.research.att.com/~smb/papers/pathalias.paper.pdf](http://www.research.att.com/~smb/papers/pathalias.paper.pdf) - 0 B

### 8. Microsoft Excel has provided some statistical analysis ...

... Spreadsheet based software have some **advantages** over the others. ... R1C1:R10C2 is a wa representing "**relative**" **addressing** oriented to the top left-hand corner ...  
[weatherhead.cwr.u.edu/msmesb/1997PennState/nagshpour.PDF](http://weatherhead.cwr.u.edu/msmesb/1997PennState/nagshpour.PDF) - 0 B

### 9. Problem Sheet 3, (x3)

... reuse a symbol in another module -- which would destroy one of the big **advantages** of havi is no base-displacement **addressing** or pc-**relative addressing**. ...  
[www-rohan.sdsu.edu/faculty/vinge/courses/fall97/cs530/x3.html](http://www-rohan.sdsu.edu/faculty/vinge/courses/fall97/cs530/x3.html) - 35 KB

### 10. Stack Computers: Appendix A -- A Survey of Computers with Hardware .....

... A stack frame pointer into the register file provides **relative addressing** of registers. ... One **advantages** to keeping the stacks resident in program memory ...  
[www.ece.cmu.edu/~koopman/stack\\_computers/appa.html](http://www.ece.cmu.edu/~koopman/stack_computers/appa.html) - 63 KB

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